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TALLINNA TEHNIKAÜLIKOOL

TALLINN UNIVERSITY OF TECHNOLOGY

# Synthesis of On-line Planning Tester for Non-deterministic EFSM Models

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# Overview

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- Scope and main idea of the work
- Workflow of testing
- Off-line preparation algorithm and example
- On-line testing algorithm and example
- Implementation and complexity issues
- Conclusions

# Scope of the work

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- Black box model based testing
  - tests are generated from the model
- Model is **non-deterministic**
  - on-line testing needed
  - output observability assumed
- Several test goals are tackled at the same time
  - minimizing the amount and length of the tests



# Testing non-deterministic models

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- Test cases cannot be prepared beforehand
- Tester must decide inputs during the test based on observed outputs and active goals
- Test planning is costly and not feasible on-line

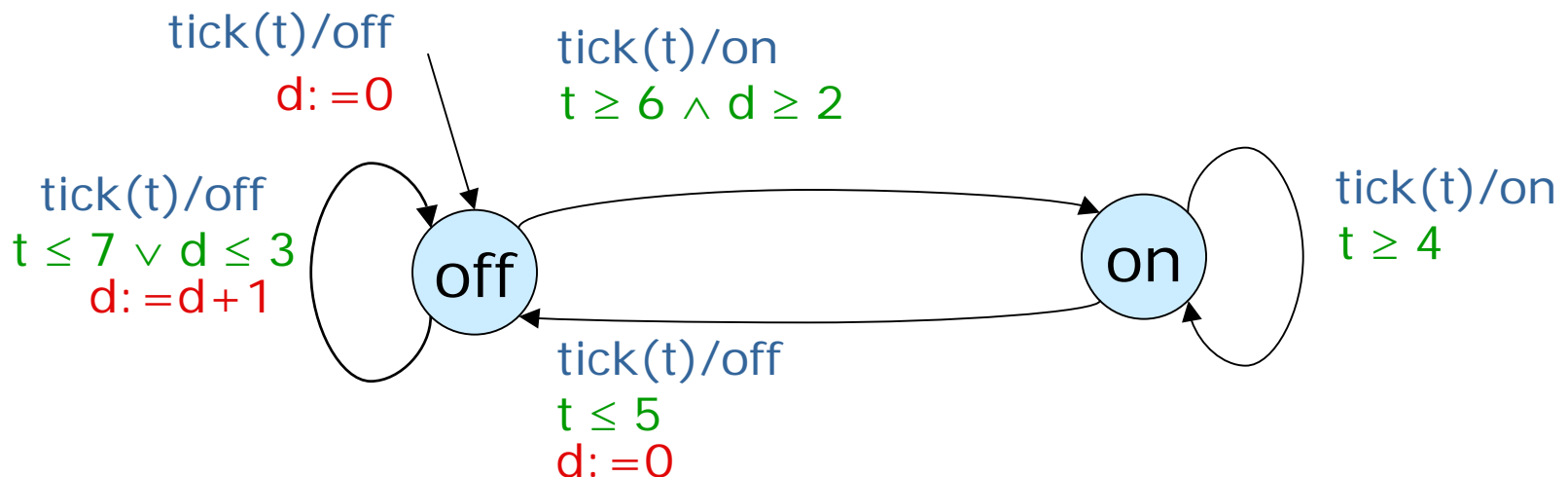
## Proposed solution

- Model is analysed off-line
- Result is expressed as a set of data constraints for each test goal
- Data instance generation is done on-line

# Model of SUT



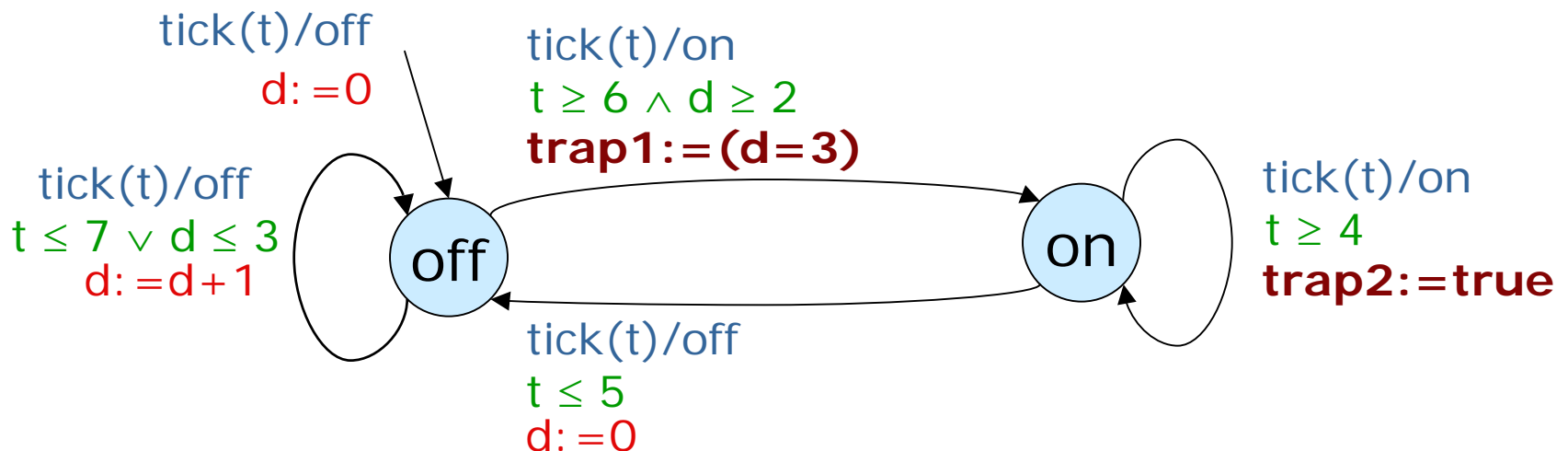
- Model is given as EFSM
  - input/output, guard, update
  - input parameter  $t$  [temp] and variable  $d$  [delay]
- Requirements
  - fridge must switch off when  $t$  is 4..5
  - fridge must switch on when  $t$  is 6..7 and it has been off 20..39 seconds (tick every 10 seconds)



# Modeling of test goals

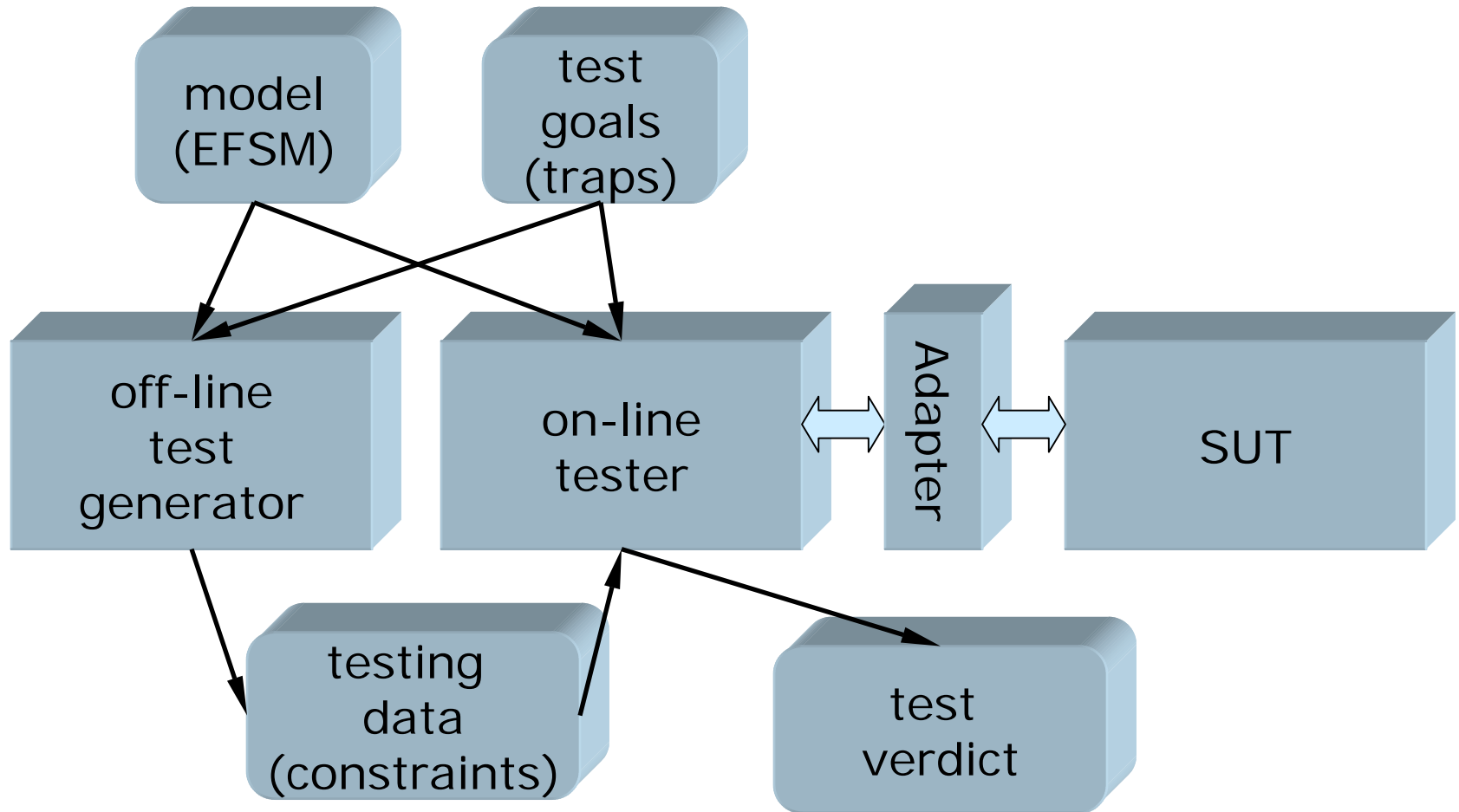


- Test goals are expressed by traps
  - trap is a pair  $\langle \text{transition}, \text{predicate} \rangle$
  - expressed as update of trap variable in model
- Can express
  - transition coverage
  - transition sequence
  - repeated pass using auxiliary variable



# Workflow

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# Constraints

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- A set of constraints is generated for every trap
  - help to guard the on-line tester towards the trap
- Constraints for states
  - Minimal path constraint  $C_s$   
condition for the shortest paths to trap  $tr$  from state  $s$
  - Maximal path constraint  $C_s^*$   
condition for all paths to trap  $tr$  from state  $s$  that extend the constraint
- Constraints for transitions
  - Minimal  $C_t$  and maximal  $C_t^*$  as for states
  - Guarding constraint  $C_t^g$   
if the shortest path to the trap starts with the transition
- Path lengths  $L_s, L_s^*, L_t$  and  $L_t^*$  are recorded also



# Offline algorithm for trap $tr$

**initialise**  $C$  to *false*,  $L$  to 0

$C_t^* = guard_t \wedge condition_{tr}$

**while** fixpoint or search *depth* is reached

**for** each state  $s$  on the depth level do

$C_s^* = \text{simplify}(C_s^{*' \vee \exists I: \forall C_{ti}^*)$  //  $ti - t$  leaving from  $s$ ;  $I - input$

**if** SAT( $\neg(C_s^* \Rightarrow C_s^{*'}$ )) //  $C_s^*$  changed

$L_s^* = depth$

**if not**  $C_s$  // minimal constraint

$C_s = C_s^*$ ;  $L_s = L_s^*$

**for** each transition  $t$  coming to  $s$

$C_t^* = \text{simplify}(C_t^{*' \vee guard_t \wedge wp(update_t, C_s^*))$

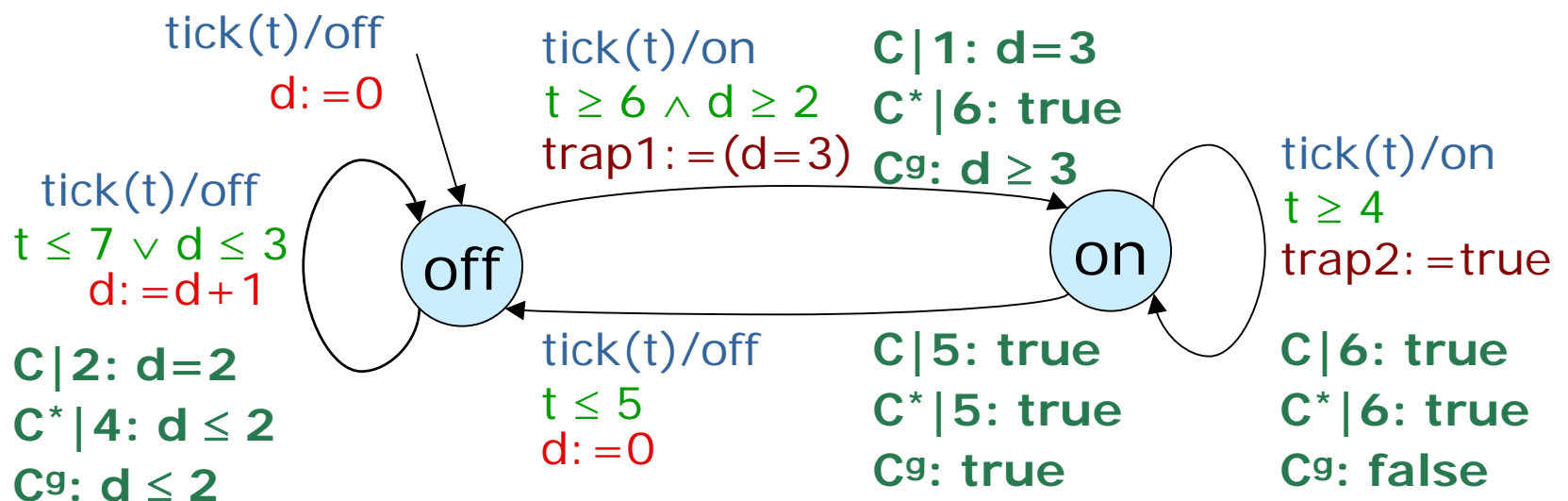
record  $L_s^*$ ,  $C_t^*$ ,  $L_s$  if needed

$C_t^g = \text{simplify}(C_t^{g' \vee (\exists I: C_t^* \wedge \neg C_{source(t)}^*))$

# Off-line constraint generation

## Constraints for trap1:

- Constraints  $C|L$  give the condition and length for the shortest path
- Constraints  $C^*|L^*$  give the condition and length for all paths up to fixpoint (or search depth)
- Constraints  $C^g$  give the condition for choosing the next transition depending on the values of variables



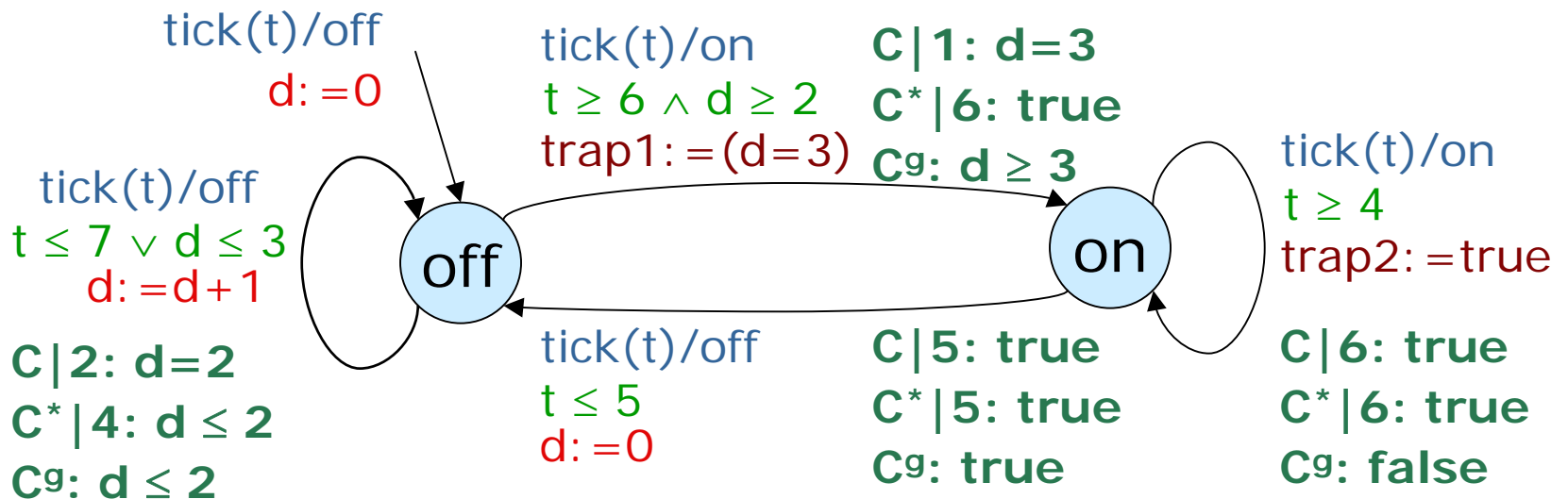
# On-line algorithm (greedy)

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```
while exist uncovered traps //at state  $s$ 
  select nearest reachable trap  $tr$  // using SAT()
  select transition with  $C_t^g$  satisfiable // using SAT()
  select input parameters valuation by
    solving  $C_t$  or  $C_t^*$  // constraint solving
  communicate the inputs to SUT
  if the output does not conform to the model // using SAT()
    stop(test_failed)
  move to the next state
end while
stop(test_passed)
```

# Example (on-line)

1. tick(true): off, d=0
2. tick(true): off, d=1
3. tick(true): off, d=2
4. tick(t < 6): off, d=3
5. tick(t ≥ 6): on, d=3 trap1☺ off, d=4
6. tick(t > 7): on, d=4
7. tick(t > 5): on, d=4 trap2☺
8. tick(t < 4): off, d=0 ↗



# Implementation issues

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- UPPAAL used for modelling (Uppsala & Aalborg U)
- Z3 SMT solver suite (Microsoft Research)
  - simplification of constraints
  - quantifier elimination
  - SAT solver
  - constraint solving (model generation)
- Python scripts for parsing and constraining generation algorithm implementation
- TestCast - TTCN3 toolset (Elvior)
  - running generated TTCN3 scripts

# Complexity issues

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- Constraints limited to decidable theories
  - linear arithmetic (+ others supported by solver)
- Theoretical limits
  - SAT problem is NP-complete
  - decision procedures and simplification of Presburger arithmetic is double-exponential
- Practical aspects
  - number of constraints is in  $O(\text{traps} * \text{transitions})$
  - Z3 does a good job in SAT and simplification
- Search depth
  - complexity of the constraints depends on the structure of the model and search depth
  - search depth can be constrained off-line when the time for the SAT check needed on-line exceeds the predefined limit

# Constrained search

16/11/20

Behaviour of the FBCU to power supply changes  
Unfolded FSM model derived from the model\_7\_3 for RPT testing

Based on the model\_7\_7 non-deterministic transitions added:  
 - to transitions which repetitive execution may invoke non-deterministic output  
 Tp1 -- Tp2ND1, Tp7ND1, Tp7N1 -- Tp19ND1, Tp19ND2, Tp14 -- Tp14ND1, Tp14ND2, Tp18 -- Tp18ND1, Tp18ND2  
 - to some transitions where power value is equal to the range bound (power level range bound)  
 updated Tp32a (voltage == HBdown) -- voltage == HBdown; added Tp34aND1, Tp34aND2  
 updated Tp32b (voltage == HBdown) -- voltage == HBdown; added Tp34bND1, Tp34bND2, Tp34bND3  
 updated Tp32c (voltage == HBdown) -- voltage == HBdown; added Tp34cND1, Tp34cND2  
 updated Tp32e (voltage == HBdown) -- voltage == HBdown; added Tp34eND1, Tp34eND2

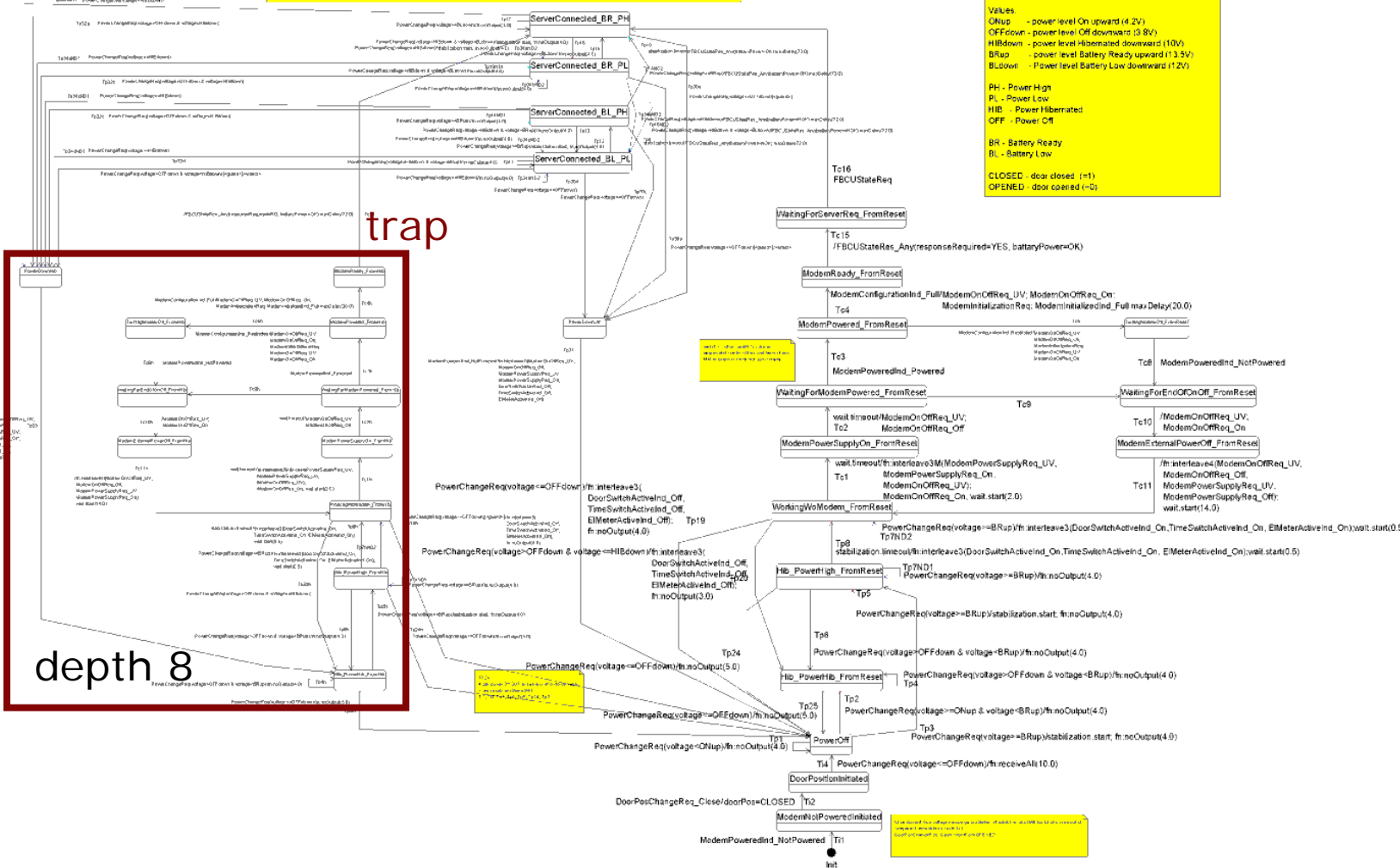
Context variables:  
 timer stabilization - Power supply stabilization period (21sec)  
 cv - current voltage level (PH, PL, HB)  
 bs - battery state (BR, BL)  
 fromReset - FBCU is going from Reset (true, false)  
 timer wait - waiting period between steps in normal working

Values:  
 ONup - power level On upward (4.2V)  
 OFFdown - power level Off downward (3.8V)  
 HBdown - power level hibernated downward (10V)  
 BRup - power level Battery Ready upward (13.5V)  
 BLdown - Power level Battery Low Downward (1.2V)

PH - Power High  
 PL - Power Low  
 HB - Power Hibernated  
 OFF - Power Off

BR - Battery Ready  
 BL - Battery Low

CLOSED - door closed (=1)  
 OPENED - door opened (=0)



trap

depth 8

# Main results

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- Tester for non-deterministic EFSM
- Efficient on-line test planning
  - supported by off-line preparation
- Off-line computation is usable also for off-line test cases generation for deterministic models
- On-line planning drives the test towards uncovered test goals resulting a test with sub-optimal length